

Notice of References Cited		Application/Control No.	Applicant(s)/Patent Under Reexamination 09/994,574 WHEELER ET AL.	
		Examiner	Art Unit	Page 1 of 1 Russ Guill 2123

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-2002/0023250	02-2002	Yumoto et al.	716/1
	B	US-			
	C	US-			
	D	US-			
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	F	US-			
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	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Samier Palnitkar, "Verilog HDL A Guide to Digital Design and Synthesis", 1996, SunSoft Press
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.